

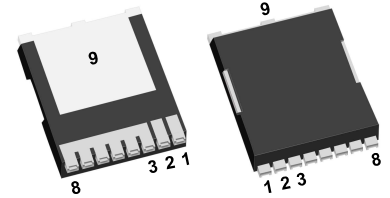
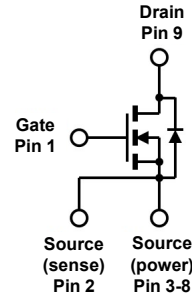
ICE10M75T Silicon Carbide Power MOSFET

Features

- 750V 9.5mohm SiC MOSFET
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast Reverse Recovery
- Optimized design for high performance power systems



Product Summary			
I_D	$T_A=25^{\circ}\text{C}$	152A	Max
$V_{(BR)DSS}$	$T_C=25^{\circ}\text{C}$	750V	Min
$r_{DS(on)}$	$V_{GS}=18\text{V}$	9.5m Ω	Typ
Q_g	$V_{DS}=500\text{V}$	140nC	Typ



TOLL

1=Gate, 2=Source(sense)
3-8 =Source (power), 9=Drain

Maximum ratings^a, at $T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$T_c=25^{\circ}\text{C}$	152	A
		$T_c=100^{\circ}\text{C}$	107	
Pulsed drain current	$I_{D, \text{pulse}}$		608	A
Maximum gate source voltage	$V_{GS(max)}$		-10/+22	V
Operational gate source voltage	$V_{GS \text{ op}}$		-5/+18	V
Power dissipation	P_{tot}	$T_c=25^{\circ}\text{C}$	416	W
		$T_c=100^{\circ}\text{C}$	208	
Storage temperature	T_{stg}		-55 to +150	$^{\circ}\text{C}$
Operating temperature	T_j		-55 to +175	$^{\circ}\text{C}$

^a Pulse width and drain current limited by T_{jmax}

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	

Thermal characteristics

Thermal resistance, junction-case	R_{thJC}		-	-	0.36	°C/W
Soldering temperature, wave soldering only allowed at leads	T_{sold}	1.6mm (0.063in.) from case for 10 s	-	-	260	°C

Electrical characteristics^a, at $T_j=25^{\circ}\text{C}$, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V$	750	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=20mA$, $T_j=25^{\circ}\text{C}$	2	3.0	4	
		$V_{DS}=V_{GS}$, $I_D=20mA$, $T_j=175^{\circ}\text{C}$	-	2.2	-	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=750V$, $V_{GS}=0V$, $T_j=25^{\circ}\text{C}$	-	0.1	10	μA
		$V_{DS}=750V$, $V_{GS}=0V$, $T_j=175^{\circ}\text{C}$	-	2	-	
Gate source leakage current	I_{GSS}	$V_{GS}=-10/+22V$, $V_{DS}=0V$	-	-	± 100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=18V$, $I_D=80A$, $T_j=25^{\circ}\text{C}$	-	9.5	13	m Ω
		$V_{GS}=18V$, $I_D=80A$, $T_j=175^{\circ}\text{C}$	-	13	-	
Gate resistance	R_G	$f=1\text{ MHz}$, $V_{AC}=30mV$	-	5.5	-	Ω

Dynamic characteristics

Input capacitance	C_{iss}	$V_{DS}=500V$, $V_{GS}=0V$, $f=100\text{ kHz}$, $V_{AC}=30mV$	-	5990	-	pF
Output capacitance	C_{oss}		-	364	-	
Reverse transfer capacitance	C_{rss}		-	12.5	-	
C_{OSS} stored energy	E_{oss}		-	61	-	μJ
Forward transconductance	g_{FS}	$V_{DS}=20V$, $I_D=80A$	-	72	-	S
Turn-on delay time	$t_{d(on)}$	$V_{DS}=500V$, $V_{GS}=-5/+18V$, $I_D=80A$, $R_G=0.5\Omega$ (External), $T_j=25^{\circ}\text{C}$	-	20	-	ns
Rise time	t_r		-	30	-	
Turn-off delay time	$t_{d(off)}$		-	46	-	
Fall time	t_f		-	11	-	
Turn-on switching energy	E_{ON}		-	321	-	μJ
Turn-off switching energy	E_{OFF}		-	232	-	

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	

Dynamic characteristics

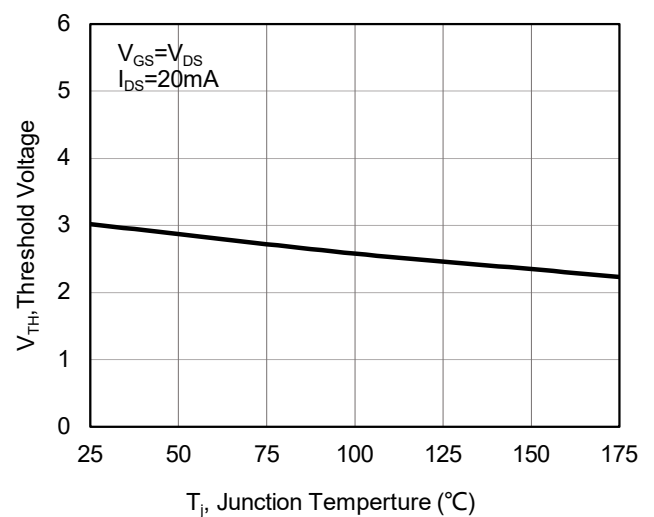
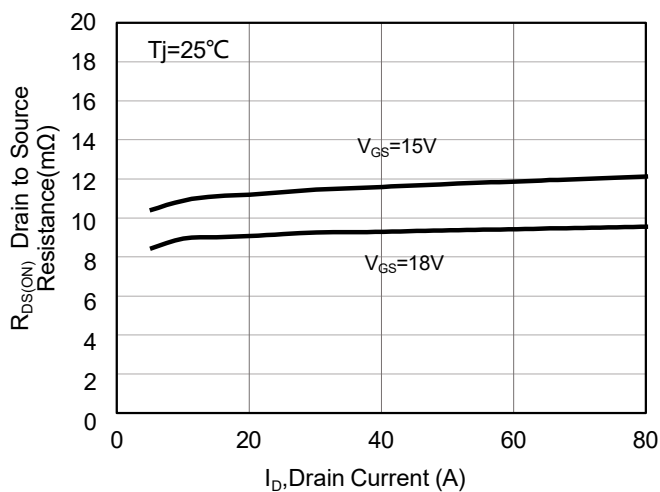
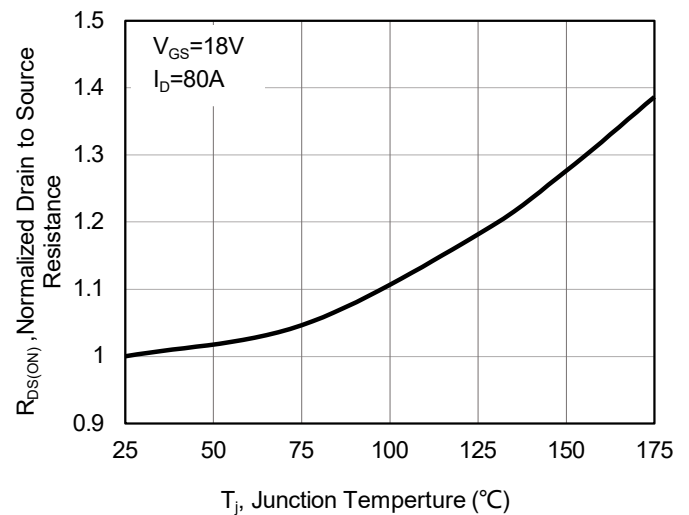
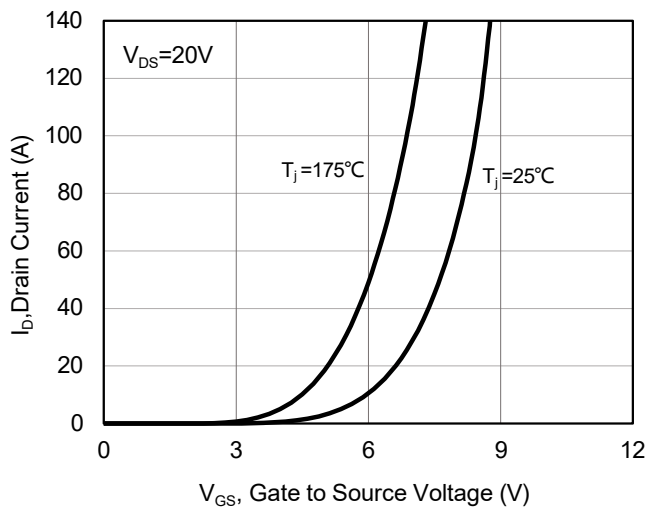
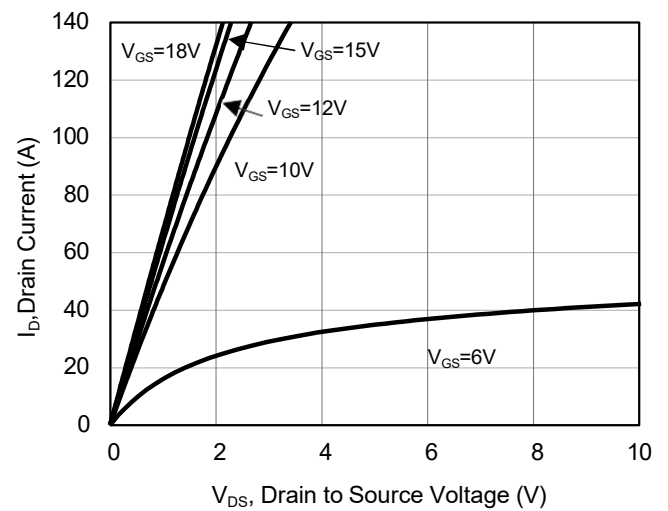
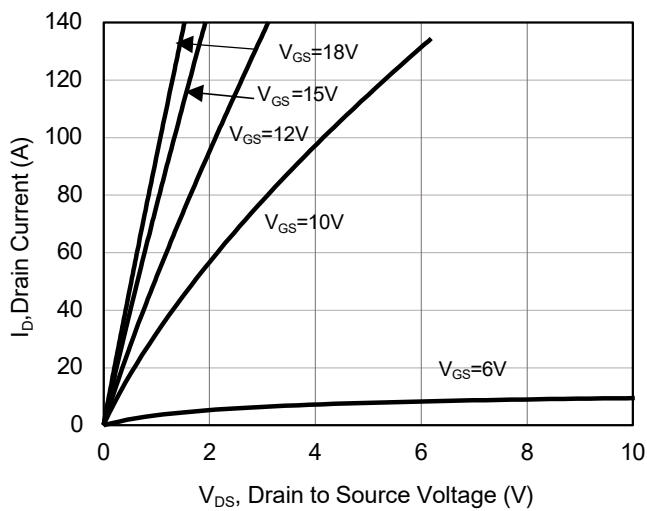
Turn-on delay time	$t_{d(on)}$	$V_{DS}=500V, V_{GS}=-5/+18V, I_D=80A, R_G=0.5\Omega$ (External), $T_j=175^\circ C$	-	18	-	ns
Rise time	t_r		-	29	-	
Turn-off delay time	$t_{d(off)}$		-	57	-	
Fall time	t_f		-	12	-	
Turn-on switching energy	E_{ON}		-	314	-	μJ
Turn-off switching energy	E_{OFF}		-	243	-	

Gate Charge Characteristics

Gate to source charge	Q_{gs}	$V_{DS}=500V, I_D=80A, V_{GS}=-5 \text{ to } +18V$	-	49	-	nC
Gate to drain charge	Q_{gd}		-	40	-	
Gate charge total	Q_g		-	140	-	

Reverse Diode

Continuous forward current	I_S	$V_{GS}=-5V$	-	-	98	A
Diode forward voltage	V_{SD}	$V_{GS}=-5V, I_{SD}=40A, T_j=25^\circ C$	-	3.8	-	V
		$V_{GS}=-5V, I_{SD}=40A, T_j=175^\circ C$	-	3.4	-	
Reverse recovery time	t_{rr}	$V_{GS}=-5V, V_{RR}=500V, I_{SD}=80A, d_i f/d_t=7600 A/\mu s, T_j=25^\circ C$	-	16	-	ns
Reverse recovery charge	Q_{rr}		-	562	-	nC
Peak reverse recovery current	I_{rm}		-	57	-	A
Reverse recovery time	t_{rr}	$V_{GS}=-5V, V_{RR}=500V, I_{SD}=80A, d_i f/d_t=9100 A/\mu s, T_j=175^\circ C$	-	25	-	ns
Reverse recovery charge	Q_{rr}		-	1272	-	nC
Peak reverse recovery current	I_{rm}		-	84	-	A



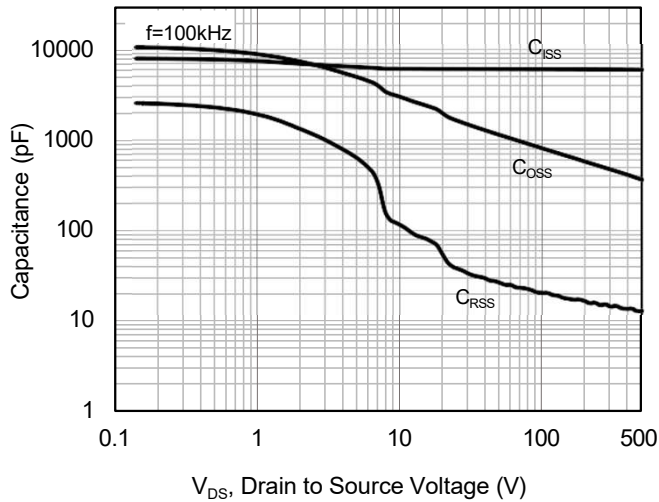


Fig 7. Capacitance Characteristics

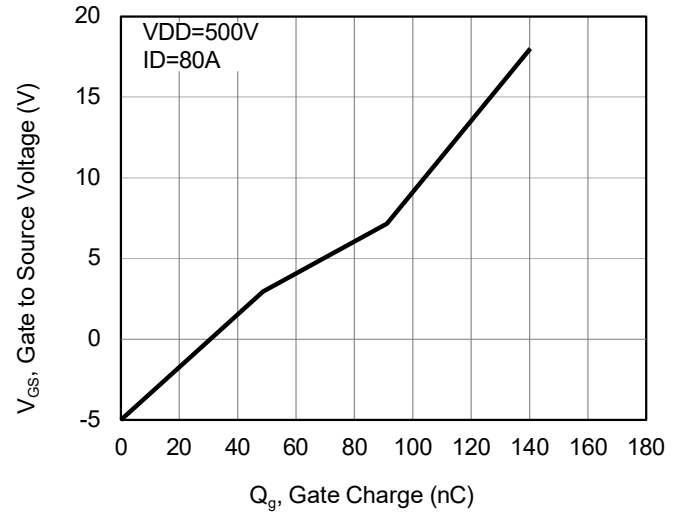


Fig 8. Gate Charge Characteristics

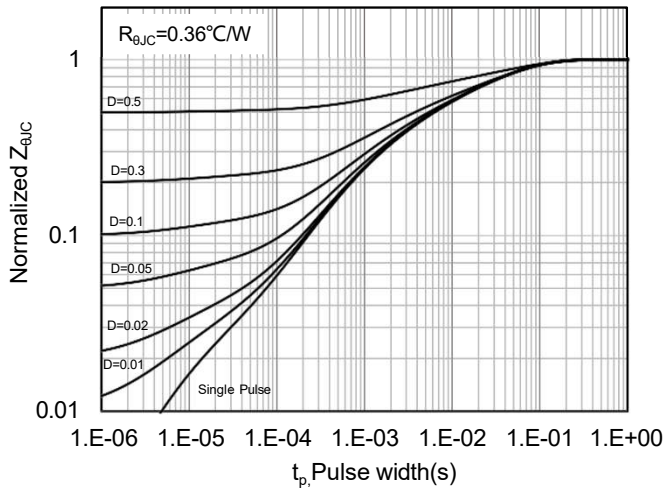


Fig 9. Transient Thermal

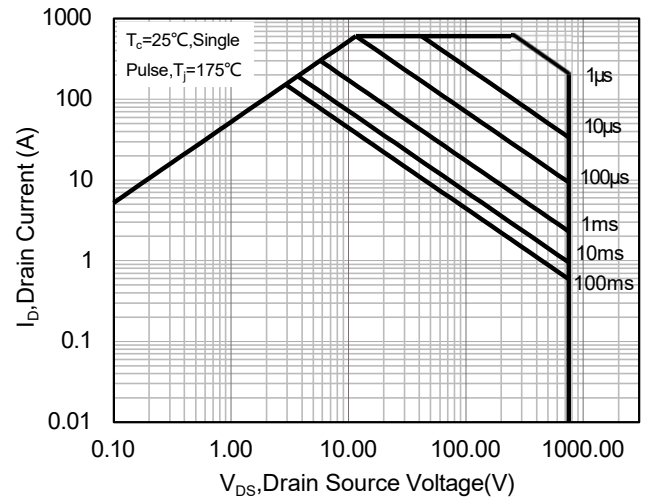


Fig 10. Safe Operating Area

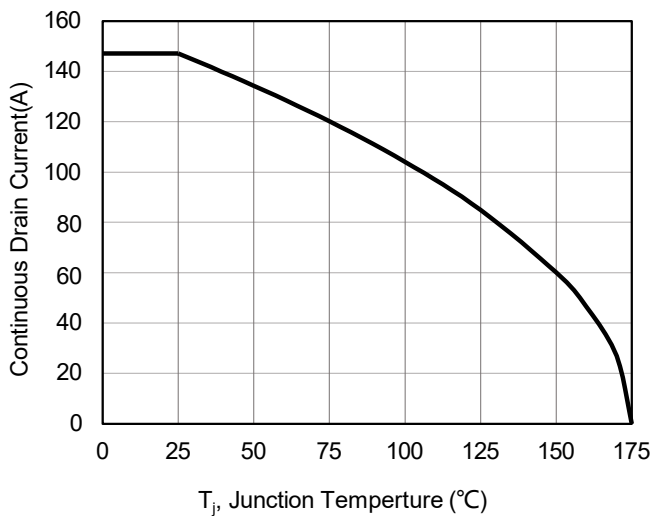


Fig 11. Continuous Drain Current Derating Vs. Case Temperature

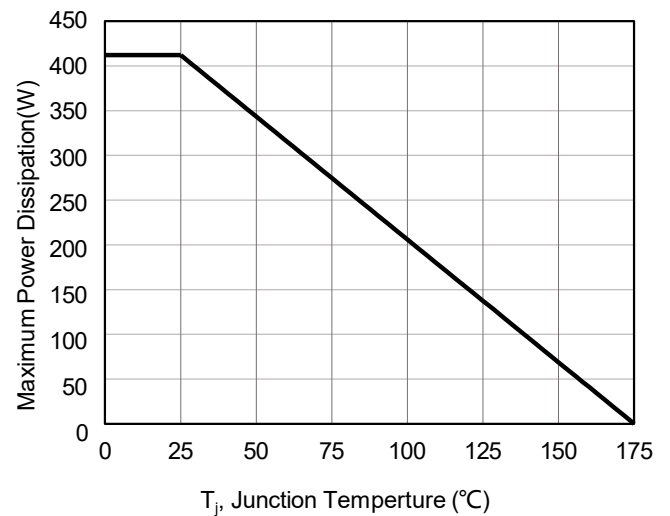


Fig 12. Maximum Power Dissipation Derating vs. Case Temperature

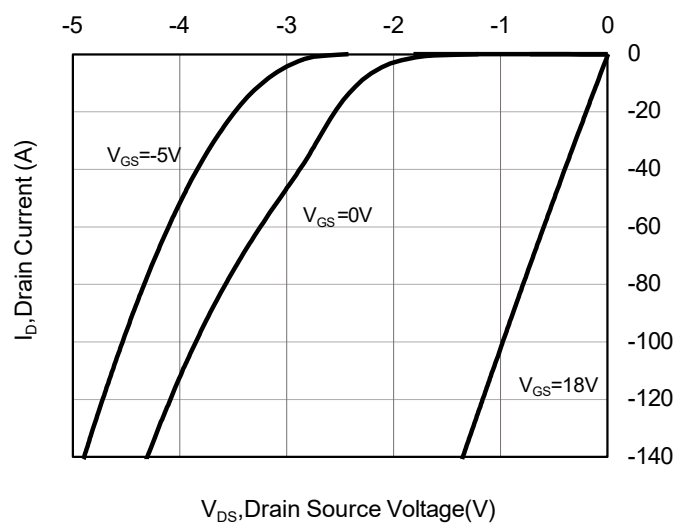


Fig 13. 3rd Quadrant Characteristic at 25 °C

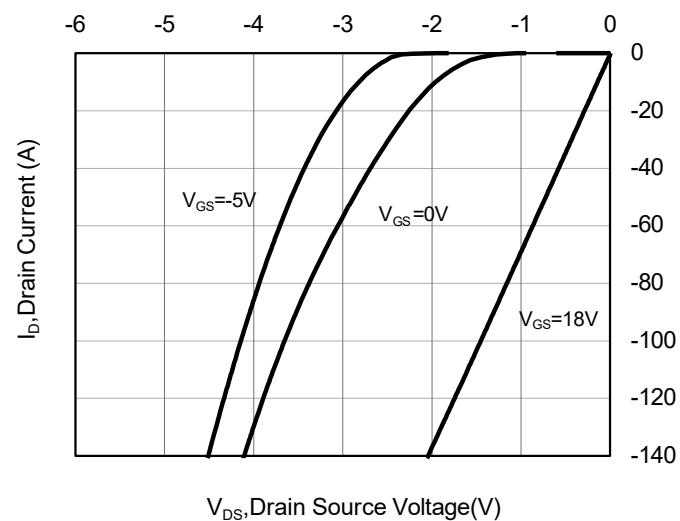
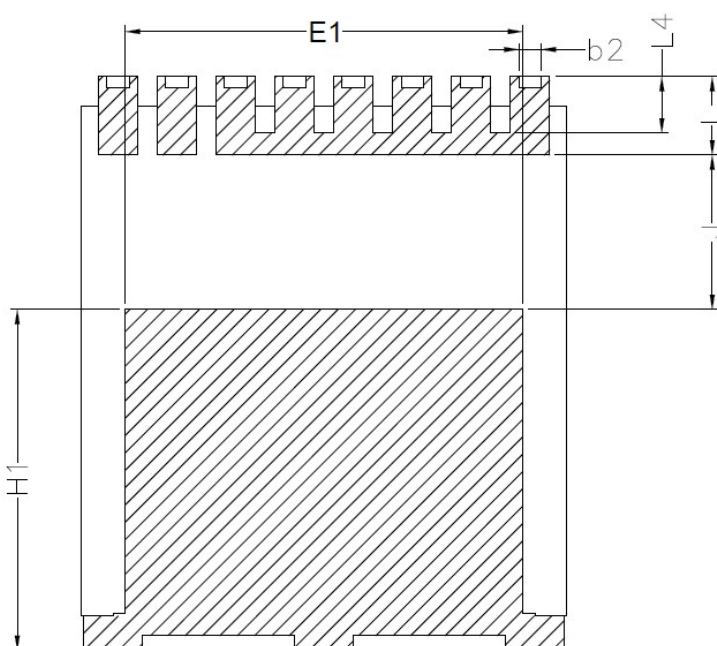
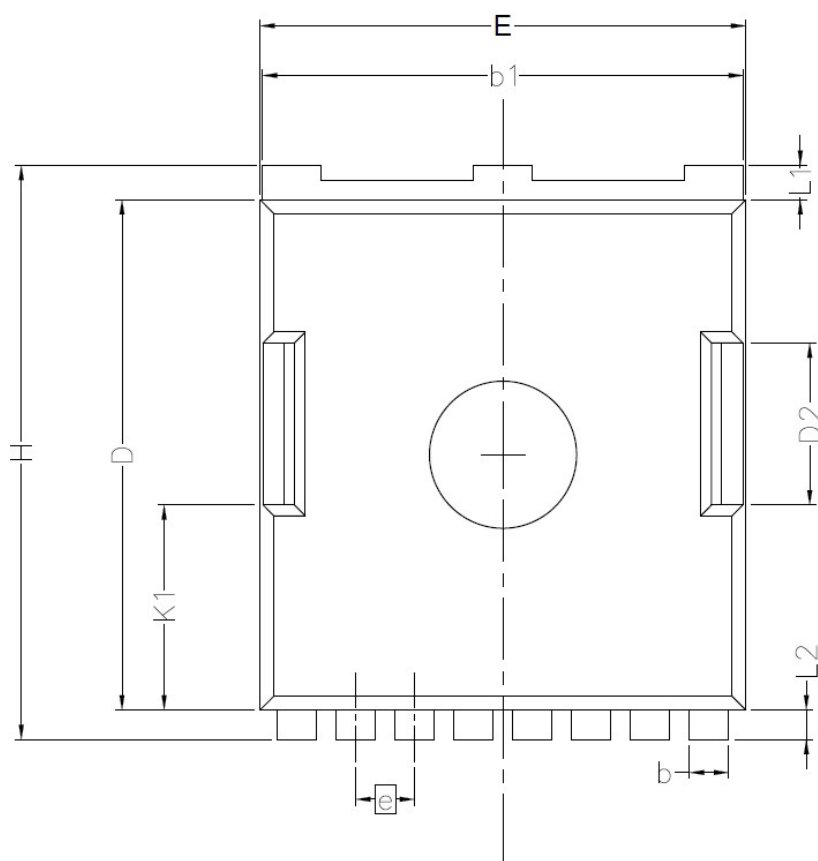
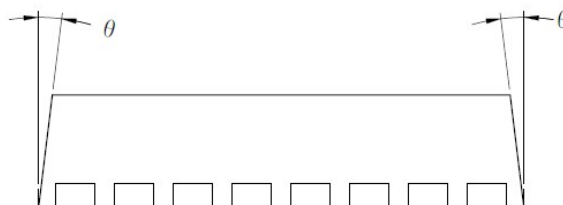
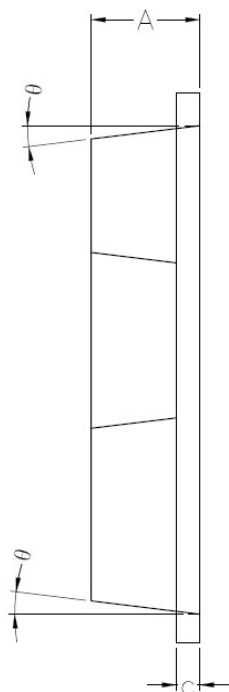


Fig 14. 3rd Quadrant Characteristic at 175 °C

Package Outline: TOLL



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Symbol	Min	Nom	Max
A	2.2	2.3	2.4
b	0.7	0.8	0.9
b1	9.7	9.8	9.9
b2	0.42	0.46	0.5
c	0.4	0.5	0.6
D	10.28	10.43	10.58
D2	3.1	3.3	3.5
E	9.7	9.9	10.1
E1	7.9	8.1	8.3
e		1.20 BSC	
H	11.48	11.68	11.88
H1	6.75	6.95	7.15
N		8	
J	3	3.15	3.3
K1	3.98	4.18	4.38
L	1.4	1.6	1.8
L1	0.6	0.7	0.8
L2	0.5	0.6	0.7
L4	1	1.15	1.3
θ	4°	6°	10°

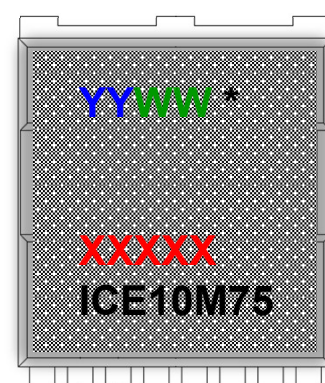
Marking Information

YY = Last two digits of the year

WW = Work week

***** = Site ID

XXXXXX = Lot ID



ICE10M75 = ICE is IceMOS logo and
10M75 is a designated device part
number

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