Preliminary

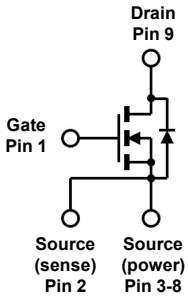
ICE26M75T

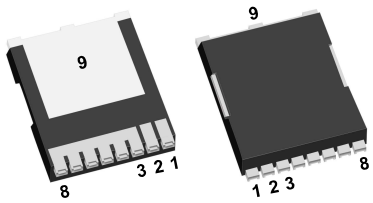
ICE26M75T Silicon Carbide Power MOSFET

Product Summary			
I_D	T_A=25°C	65A	Max.
$V_{(BR)DSS}$	T_C=25°C	750V	Min.
$r_{DS(on)}$	V_{GS}=18V	26mΩ	Typ.
Q_g	V_{DS}=500V	73nC	Typ.

Features

- 750V 26mohm SiC MOSFET
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast Reverse Recovery
- Industrial Grade SiC MOSFET Qualified by Jedec
- Optimized design for high performance power systems





TOLL

1=Gate, 2=Source(sense)

3-8 =Source (power), 9=Drain

RoHS COMPLIANT
RoHS (EU) 2015/863

CE

HALOGEN FREE

Maximum ratings [a] at T_j=25°C, unless otherwise specified

Parameter	*Symbol*	*Conditions*	*Value*	*Unit*
Continuous drain current	I_D	T_c=25°C T_c=100°C	65 46	A
Pulsed drain current	$I_{D,\ pulse}$	T_c=25°C	253	A
Maximum gate source voltage	$V_{GS(max)}$		-10/+22	V
Operational gate source voltage	$V_{GS\ op}$		-5/+18	V
Power dissipation	P_{tot}	T_c=25°C	200	W
		T_c=100°C	100	
Storage temperature	T_{stg}		-55 to +150	°C
Operating temperature	T_j		-55 to +175	°C

a limited by Tj_{max}

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	
Thermal characteristics						
Thermal resistance, junction-case	R_{thJC}		-	-	0.75	°C/W
Soldering temperature, wave soldering only allowed at leads	T_{sold}	1.6mm (0.063in.) from case for 10 s	-	-	260	°C

Electrical characteristics [a], at T_j=25°C, unless otherwise specified

Static characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS}=0V	750	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=10mA, T_j=25°C	2	2.9	4	V
		V_{DS}=V_{GS}, I_D=10mA, T_j=175°C	-	2.1	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=750V, V_{GS}=0V, T_j=25°C	-	0.1	50	µA
		V_{DS}=750V, V_{GS}=0V, T_j=175°C	-	1	-	µA
Gate source leakage current	I_{GSS}	V_{GS}=-10/+22V, V_{DS}=0V	-	-	±100	nA
Drain-source on-state resistance	$R_{DS\ (on)}$	V_{GS}=18V, I_D=40A, T_j=25°C	-	26	35	mΩ
		V_{GS}=18V, I_D=40A, T_j=175°C	-	35	-	mΩ
Gate resistance	R_G	f=1 MHz, V_{AC}=30mV	-	2	-	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V_{DS}=500V, V_{GS}=0V, f=100 kHz, V_{AC}=30mV	-	2206	-	pF
Output capacitance	C_{oss}		-	145	-	pF
Reverse transfer capacitance	C_{rss}		-	6	-	pF
C_{OSS} stored energy	E_{oss}		-	23	-	µJ
Forward transconductance	g_{FS}	V_{DS}=20V, I_D=40A	-	28	-	S
Turn-on delay time	$t_{d(on)}$	V_{DS}=500V, V_{GS}=-5/+18V, I_D=40A, R_G=4.7Ω (External), T_j=25°C	-	10	-	ns
Rise time	t_r		-	13	-	ns
Turn-off delay time	$t_{d(off)}$		-	17	-	ns
Fall time	t_f		-	9	-	ns
Turn-on switching energy	E_{ON}		-	81	-	µJ
Turn-off switching energy	E_{OFF}		-	16	-	µJ

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	
Dynamic characteristics						
Turn-on delay time	$t_{d(on)}$	V_{DS}=500V, V_{GS}=-5/+18V, I_D=40A, R_G=4.7Ω (External), T_j=175°C	-	9	-	ns
Rise time	t_r		-	13	-	
Turn-off delay time	$t_{d(off)}$		-	20	-	
Fall time	t_f		-	9	-	
Turn-on switching energy	E_{ON}		-	75	-	µJ
Turn-off switching energy	E_{OFF}		-	15	-	
Gate Charge Characteristics						
Gate to source charge	Q_{gs}	V_{DS}=500V, I_D=40A, V_{GS}=-5 to +18V	-	18	-	nC
Gate to drain charge	Q_{gd}		-	17	-	
Gate charge total	Q_g		-	73	-	
Reverse Diode						
Continuous forward current	I_S	V_{GS}=-5V	-	47	-	A
Diode forward voltage	V_{SD}	V_{GS}=-5V, I_{SD}=20A, T_j=25°C	-	3.9	-	V
		V_{GS}=-5V, I_{SD}=20A, T_j=175°C	-	3.4	-	
Reverse recovery time	t_{rr}	V_{GS}=-5V, V_{RR}=500V, I_{SD}=40A, d_{iF}/d_t=9180 A/µs, T_j=25°C	-	8	-	ns
Reverse recovery charge	Q_{rr}		-	170	-	nC
Peak reverse recovery current	I_{rm}		-	35	-	A
Reverse recovery time	t_{rr}	V_{GS}=-5V, V_{RR}=500V, I_{SD}=40A, d_{iF}/d_t=10840 A/µs, T_j=175°C	-	11	-	ns
Reverse recovery charge	Q_{rr}		-	380	-	nC
Peak reverse recovery current	I_{rm}		-	52	-	A

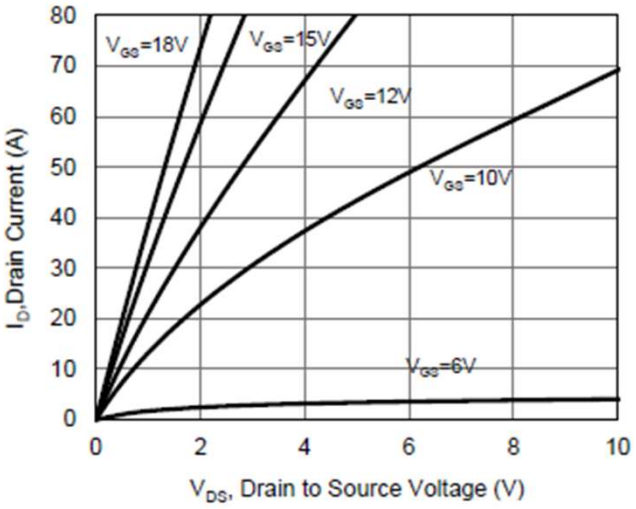


Fig 1. Output Characteristics, T_j=25°C

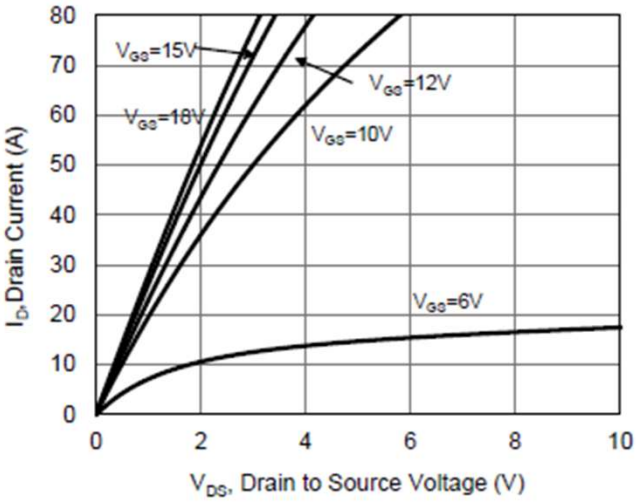


Fig 2. Output Characteristics, T_j=175°C

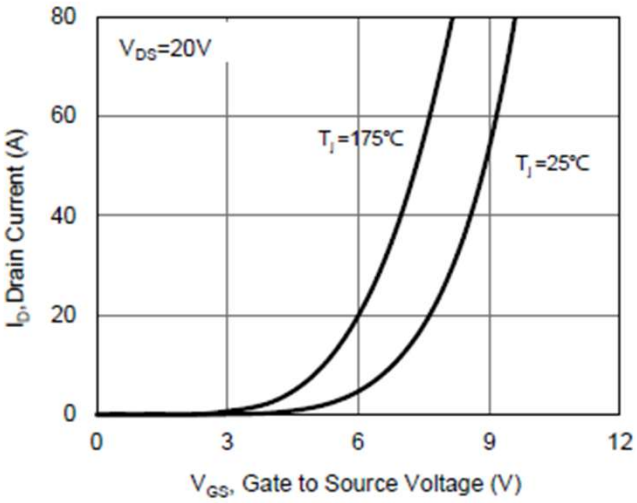


Fig 3. Transfer Characteristics

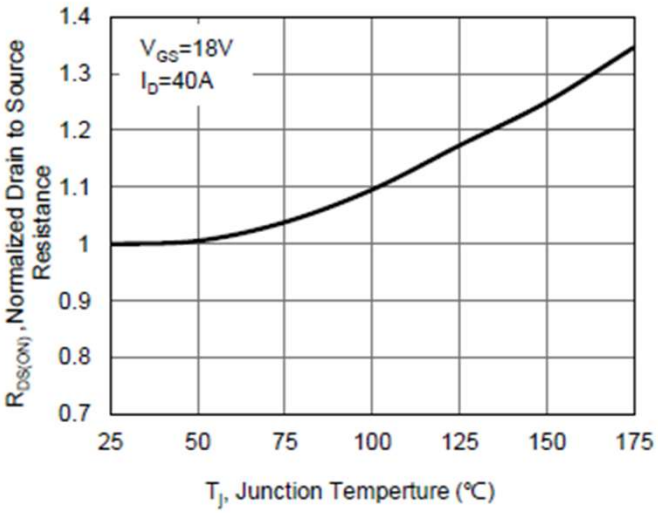


Fig 4. On-Resistance Variation with Temperature

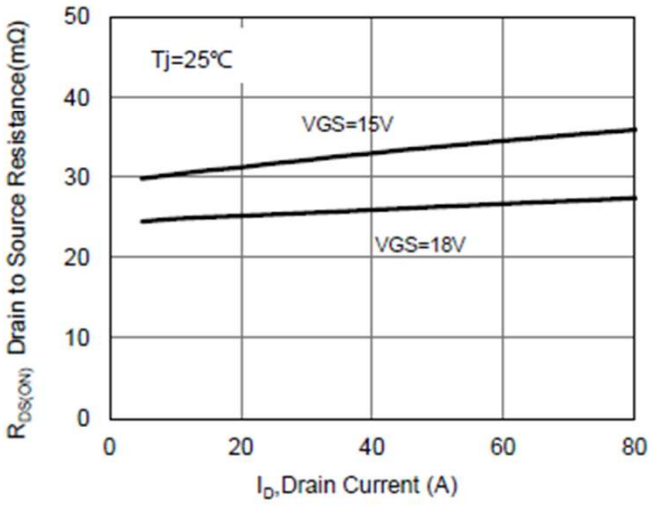


Fig 5. On-Resistance vs Drain Current

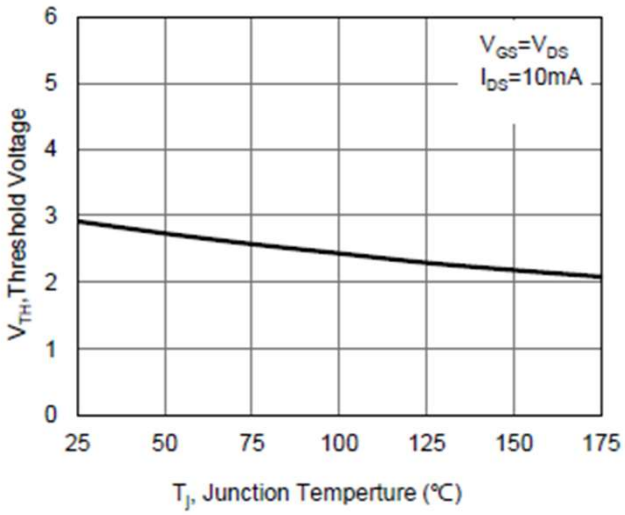


Fig 6. Gate Threshold vs Temperature

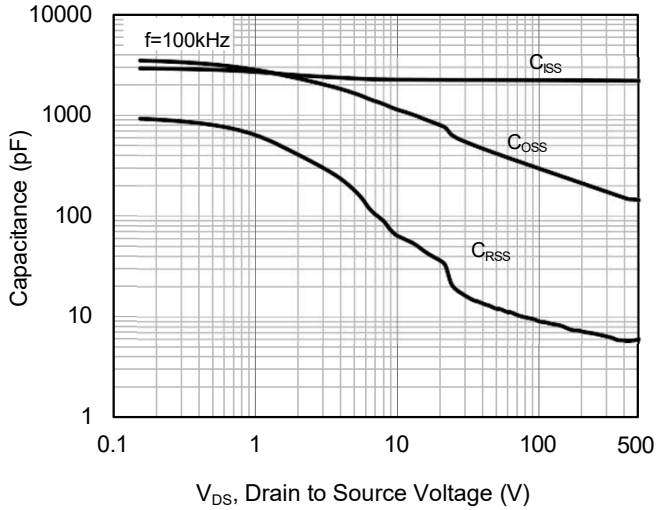


Fig 7. Capacitance Characteristics

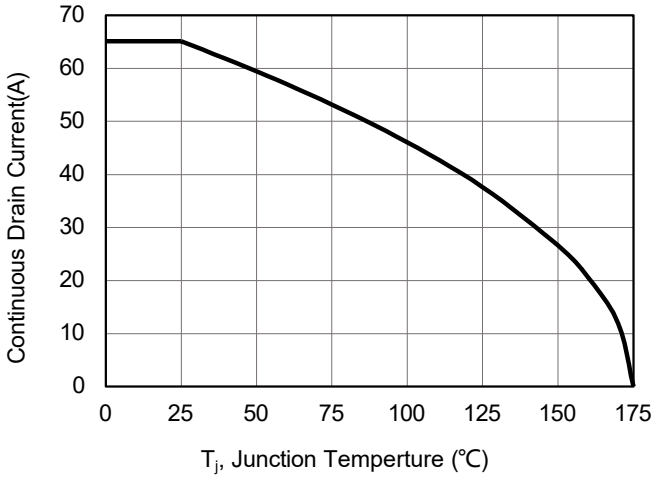


Fig 8. Continuous Drain Current Derating Vs. Case Temperature

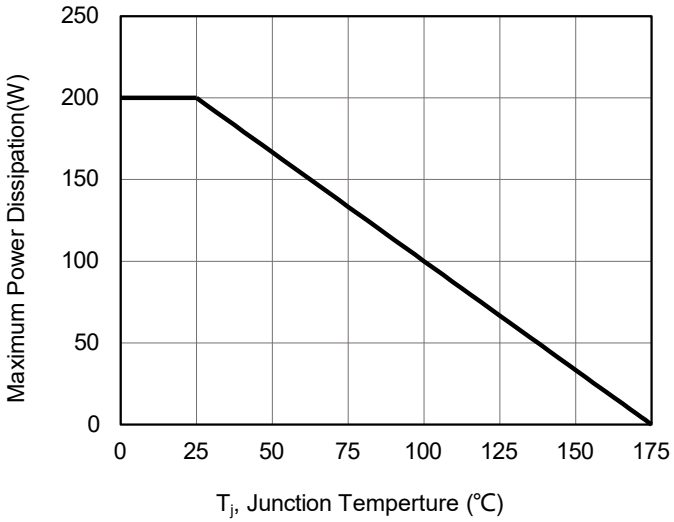


Fig 9. Maximum Power Dissipation Derating vs. Case Temperature

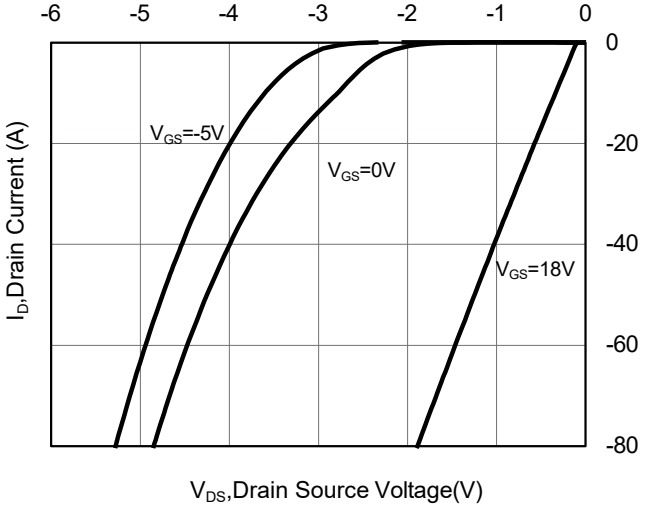


Fig 10. 3rd Quadrant Characteristic at 25 ºC

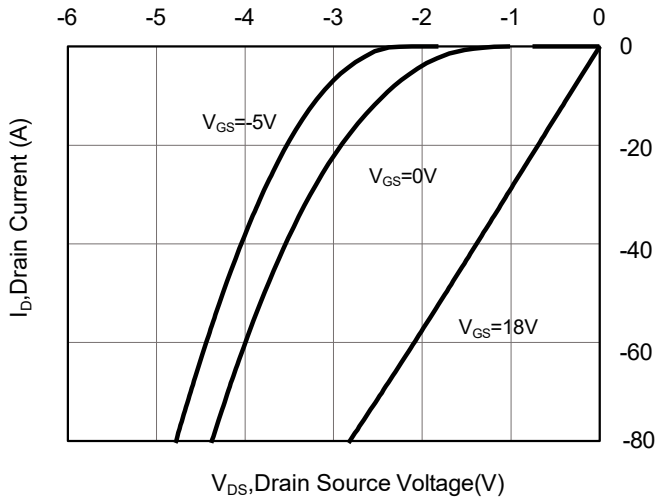
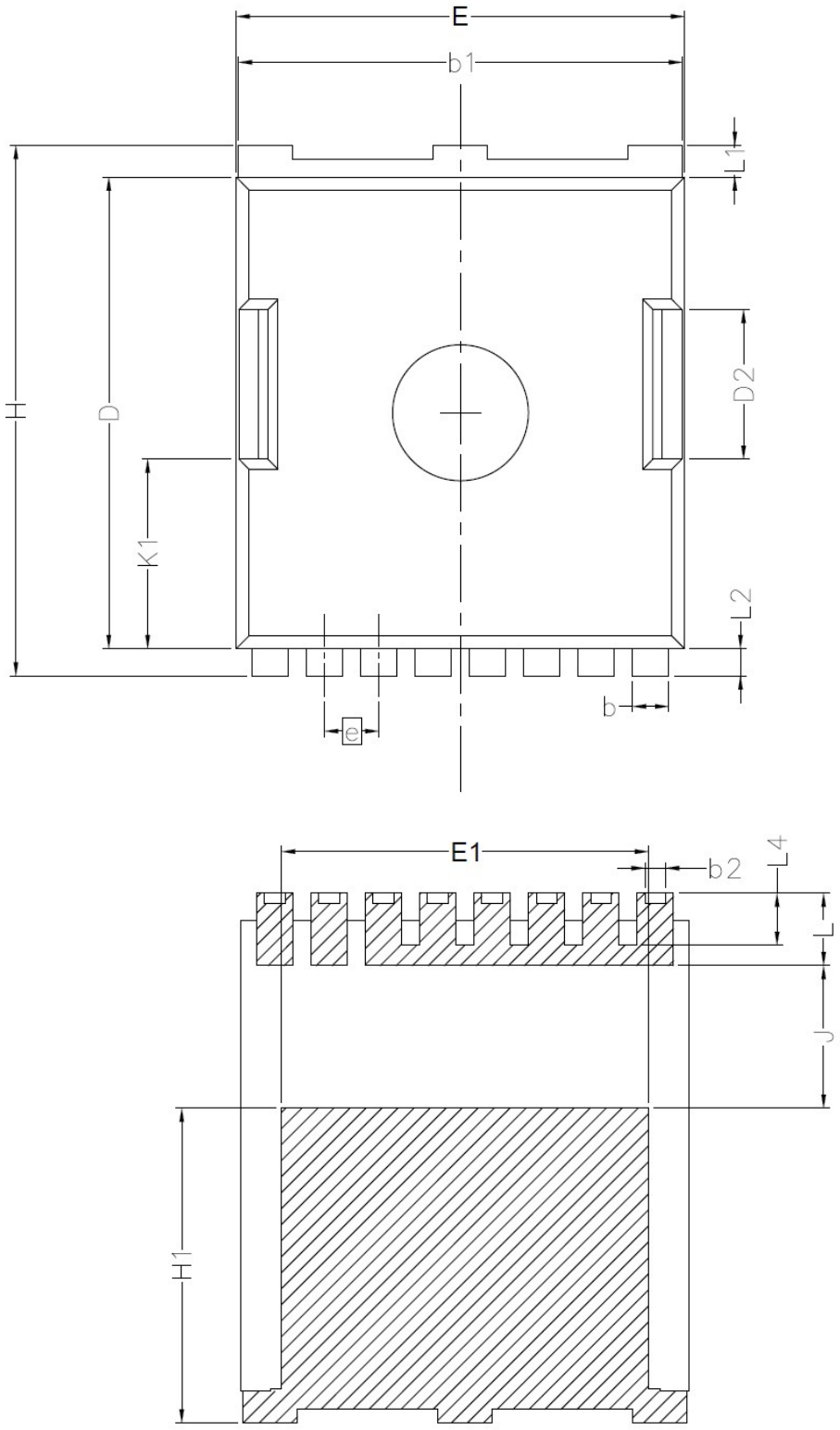
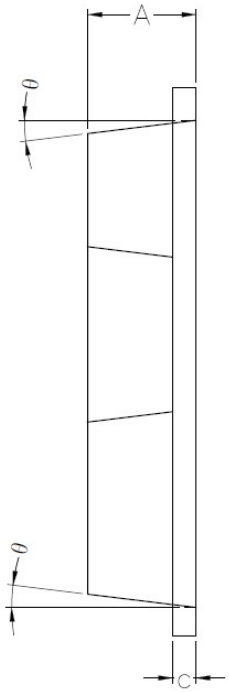


Fig 11. 3rd Quadrant Characteristic at 175 ºC

Package Outline: TOLL



Package Outline: TOLL



Symbol	Min	Nom	Max
A	2.2	2.3	2.4
b	0.7	0.8	0.9
b1	9.7	9.8	9.9
b2	0.42	0.46	0.5
c	0.4	0.5	0.6
D	10.28	10.43	10.58
D2	3.1	3.3	3.5
E	9.7	9.9	10.1
E1	7.9	8.1	8.3
e		1.20 BSC	
H	11.48	11.68	11.88
H1	6.75	6.95	7.15
N		8	
J	3	3.15	3.3
K1	3.98	4.18	4.38
L	1.4	1.6	1.8
L1	0.6	0.7	0.8
L2	0.5	0.6	0.7
L4	1	1.15	1.3
θ	4°	6°	10°

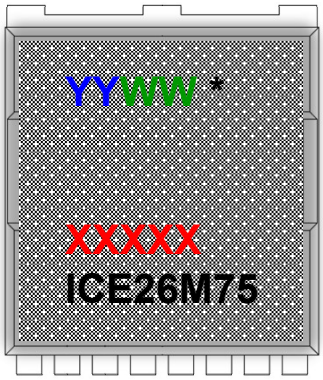
Marking Information

YY = Last two digits of the year

WW = Work week

* = Site ID

XXXXX = Lot ID



ICE26M75 = ICE is Icemos logo and 26M75 is a designated device part number

Disclaimer