

ICE115M120W

Silicon Carbide

Power MOSFET

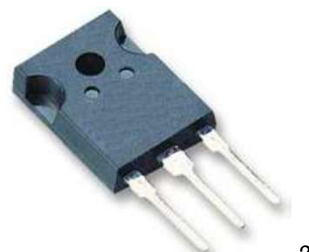
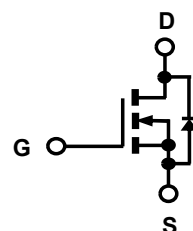
Product Summary			
I_D	$T_A=25^{\circ}\text{C}$	19.9A	Max
$V_{(BR)DSS}$	$T_C=25^{\circ}\text{C}$	1200V	Min
$r_{DS(on)}$	$V_{GS}=18\text{V}$	115m Ω	Typ
Q_g	$V_{DS}=800\text{V}$	48nC	Typ

Features

- 1200V 115mohm SiC MOSFET
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast Reverse Recovery
- Optimized design for high performance power systems



Lead Free



TO247
1:G, 2:D,
3:S

(TO-247)

Maximum ratings^a, at $T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$T_c=25^{\circ}\text{C}$	19.9	A
		$T_c=100^{\circ}\text{C}$	14	
Pulsed drain current	$I_{D, \text{pulse}}$		64	A
Maximum gate source voltage	$V_{GS(\text{max})}$		-10/+22	V
Operational gate source voltage	$V_{GS \text{ op}}$		-5/+18	V
Power dissipation	P_{tot}	$T_c=25^{\circ}\text{C}$	110.2	W
		$T_c=100^{\circ}\text{C}$	55.1	
Storage temperature	T_{stg}		-55 to +150	$^{\circ}\text{C}$
Operating temperature	T_j		-55 to +175	

^a Pulse width limited by $T_{j\text{max}}$

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	

Thermal characteristics

Thermal resistance, junction-case	R_{thJC}		-	-	1.36	°C/W
Soldering temperature, wave soldering only allowed at leads	T_{sold}	1.6mm (0.063in.) from case for 10 s	-	-	260	°C

Electrical characteristics^a, at $T_j=25^{\circ}\text{C}$, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	1200	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=2.8mA, T_j=25^{\circ}\text{C}$	2	2.7	4	
		$V_{DS}=V_{GS}, I_D=2.8mA, T_j=175^{\circ}\text{C}$	-	1.8	-	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=1200V, V_{GS}=0V, T_j=25^{\circ}\text{C}$	-	0.1	50	μA
		$V_{DS}=1200V, V_{GS}=0V, T_j=175^{\circ}\text{C}$		1		
Gate source leakage current	I_{GSS}	$V_{GS}=-10/+22V, V_{DS}=0V$	-	-	± 100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=18V, I_D=10A, T_j=25^{\circ}\text{C}$	-	115	149	m Ω
		$V_{GS}=18V, I_D=10A, T_j=175^{\circ}\text{C}$	-	214	-	
Gate resistance	R_G	$f=1\text{ MHz}, V_{AC}=30mV$	-	5	-	Ω

Dynamic characteristics

Input capacitance	C_{iss}	$V_{DS}=800V, V_{GS}=0V, f=100\text{ kHz}, V_{AC}=30mV$	-	625	-	pF
Output capacitance	C_{oss}		-	37	-	
Reverse transfer capacitance	C_{rss}		-	4.8	-	
C_{OSS} stored energy	E_{oss}		-	14	-	μJ
Forward transconductance	g_{FS}	$V_{DS}=20V, I_D=10A$	-	6.4	-	S
Turn-on delay time	$t_{d(on)}$	$V_{DS}=800V, V_{GS}=-5/+18V, I_D=10A, R_G=10\Omega$ (External), $T_j=25^{\circ}\text{C}$	-	11	-	ns
Rise time	t_r		-	14	-	
Turn-off delay time	$t_{d(off)}$		-	16	-	
Fall time	t_f		-	14	-	
Turn-on switching energy	E_{ON}		-	160	-	μJ
Turn-off switching energy	E_{OFF}		-	22	-	

Parameter	Symbol	Conditions	Values			Unit
			Min	Typ	Max	

Dynamic characteristics

Turn-on delay time	$t_{d(on)}$	$V_{DS}=800V, V_{GS}=-5/+18V,$ $I_D=10A, R_G=10\Omega$ (External) $T_j=175^\circ C$	-	9	-	ns
Rise time	t_r		-	15	-	
Turn-off delay time	$t_{d(off)}$		-	20	-	
Fall time	t_f		-	15	-	
Turn-on switching energy	E_{ON}		-	239	-	μJ
Turn-off switching energy	E_{OFF}		-	21	-	

Gate Charge Characteristics

Gate to source charge	Q_{gs}	$V_{DS}=800V, I_D=10A,$ $V_{GS}=-5$ to $+18V$	-	20	-	nC
Gate to drain charge	Q_{gd}		-	8	-	
Gate charge total	Q_g		-	48	-	

Reverse Diode

Continuous forward current	I_S	$V_{GS}=-5V$	-	19.9	-	A
Diode forward voltage	V_{SD}	$V_{GS}=-5V, I_{SD}=5A, T_j=25^\circ C$	-	3.5	-	V
		$V_{GS}=-5V, I_{SD}=5A, T_j=175^\circ C$	-	3.0	-	
Reverse recovery time	t_{rr}	$V_{GS}=-5V, V_{RR}=800V,$ $R_{G(ext)}=10\Omega, I_{SD}=10A,$ $d_{IF}/d_t=TBD A/\mu s, T_j=25^\circ C$	-	21	-	ns
Reverse recovery charge	Q_{rr}		-	173	-	nC
Peak reverse recovery current	I_{rm}		-	14	-	A
Reverse recovery time	t_{rr}	$V_{GS}=-5V, V_{RR}=800V,$ $R_{G(ext)}=10\Omega, I_{SD}=10A,$ $d_{IF}/d_t=TBD A/\mu s, T_j=175^\circ C$	-	21	-	ns
Reverse recovery charge	Q_{rr}		-	356	-	nC
Peak reverse recovery current	I_{rm}		-	24	-	A

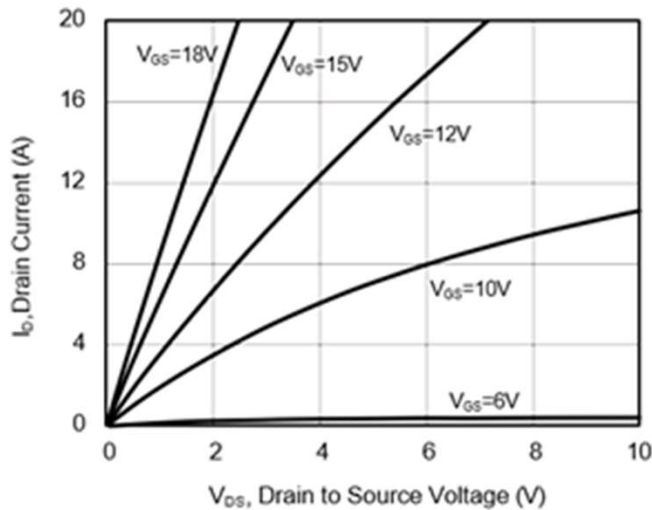


Fig 1. Output Characteristics, $T_j = -55^\circ\text{C}$

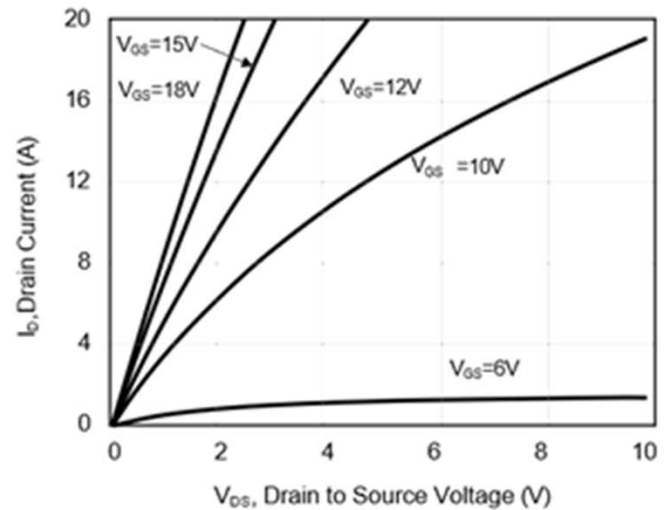


Fig 2. Output Characteristics, $T_j = 25^\circ\text{C}$

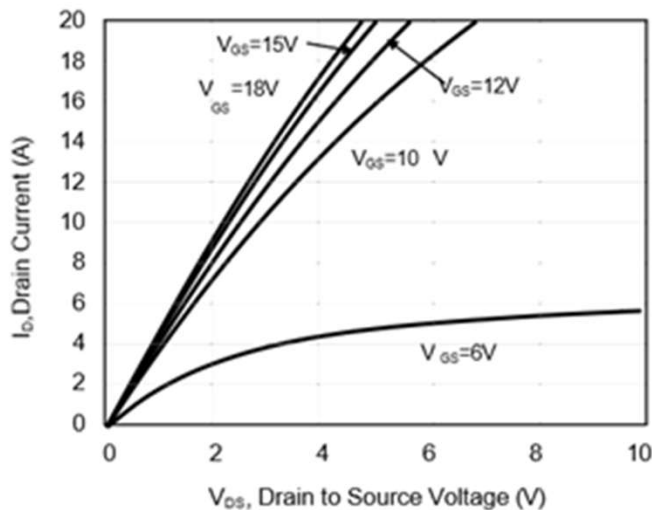


Fig 3. Output Characteristics, $T_j = 175^\circ\text{C}$

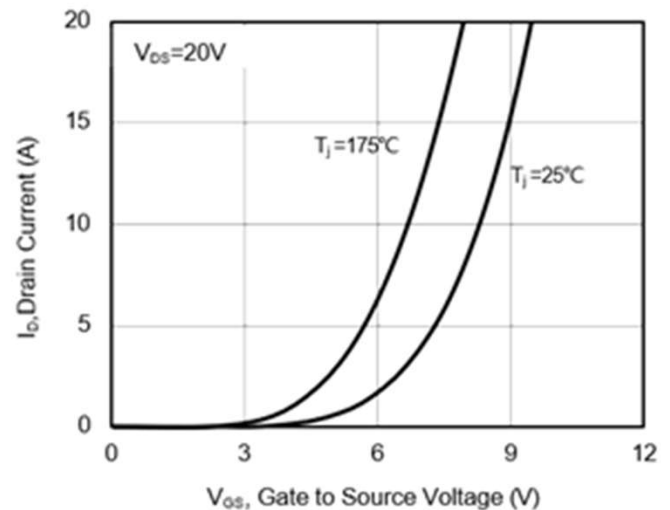


Fig 4. Transfer Characteristics

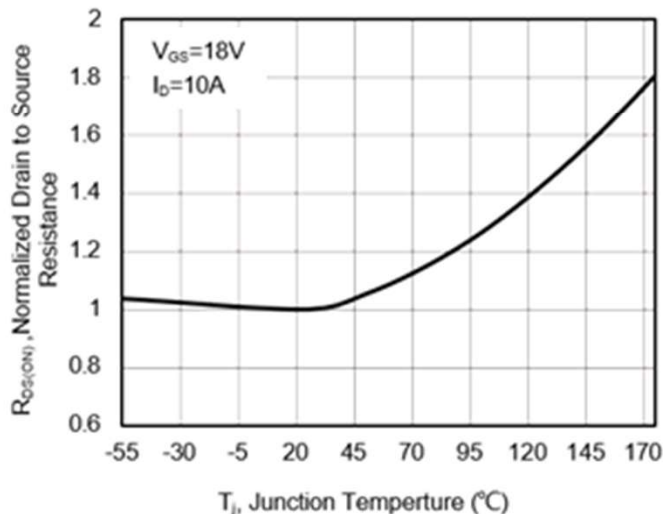


Fig 5. On-Resistance Variation with Temperature

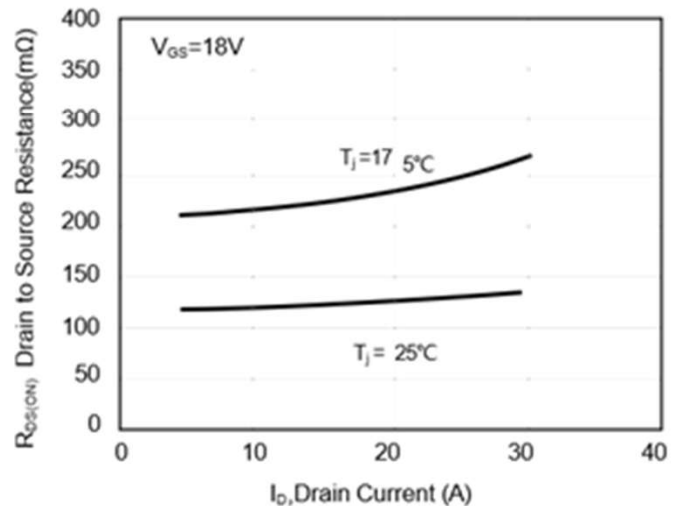


Fig 6. On-Resistance vs Drain Current

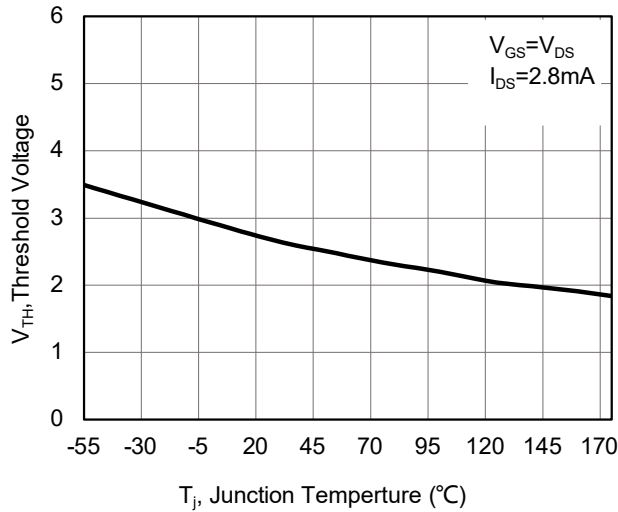


Fig 7. Gate Threshold vs Temperature

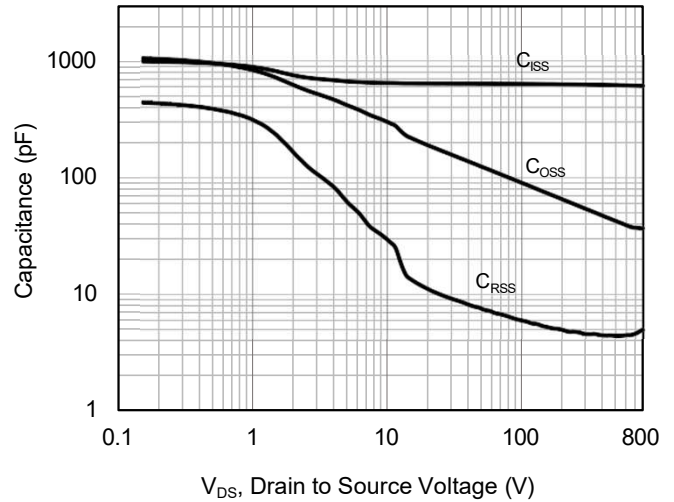


Fig 8. Capacitance Characteristics

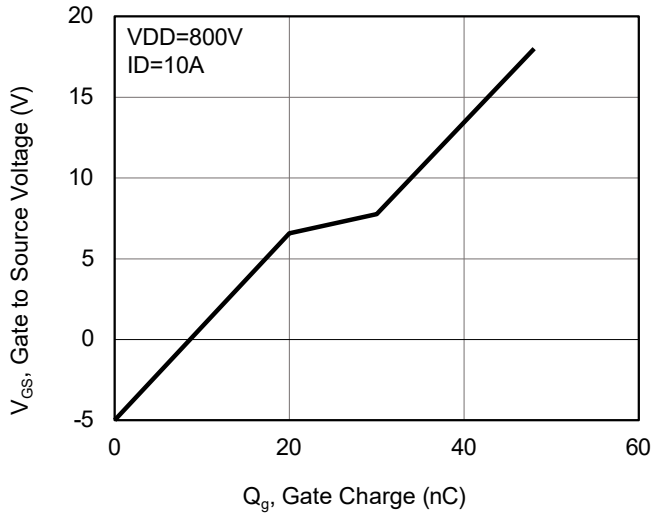


Fig 9. Gate Charge Characteristics

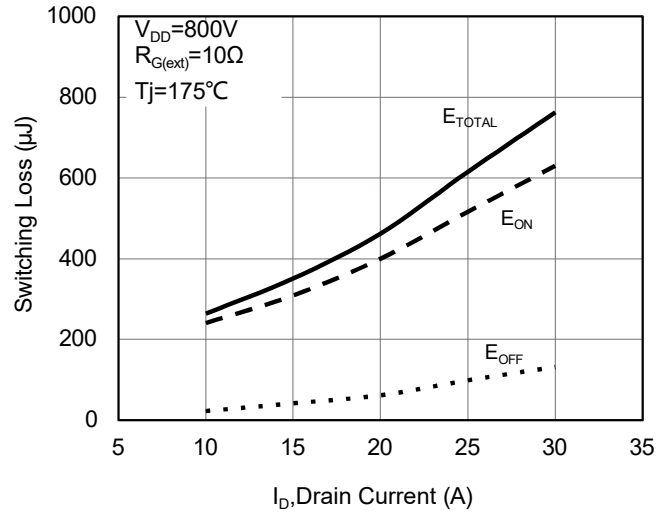


Fig 10. Switching Energy vs. Drain Current

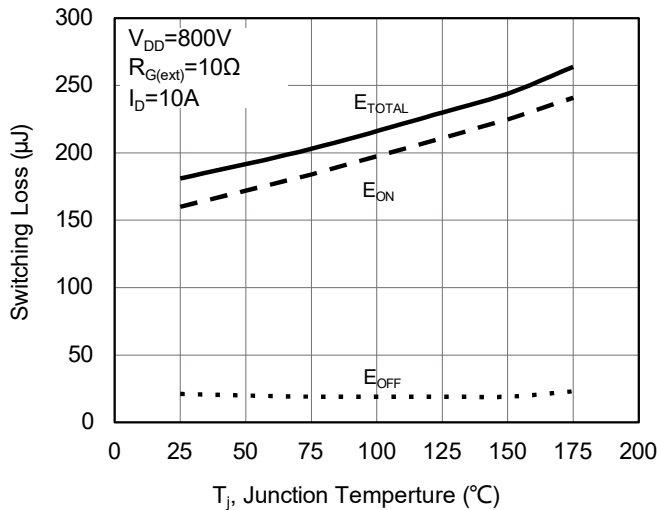


Fig 11. Switching Energy vs. Temperature

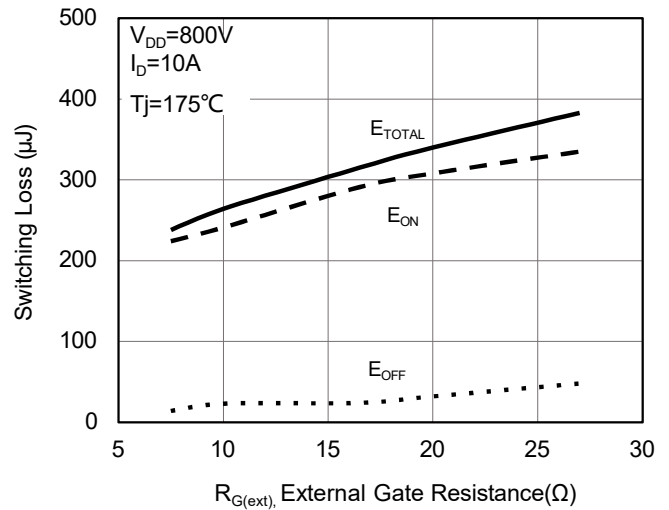


Fig 12. Switching Energy vs. Gate Resistance

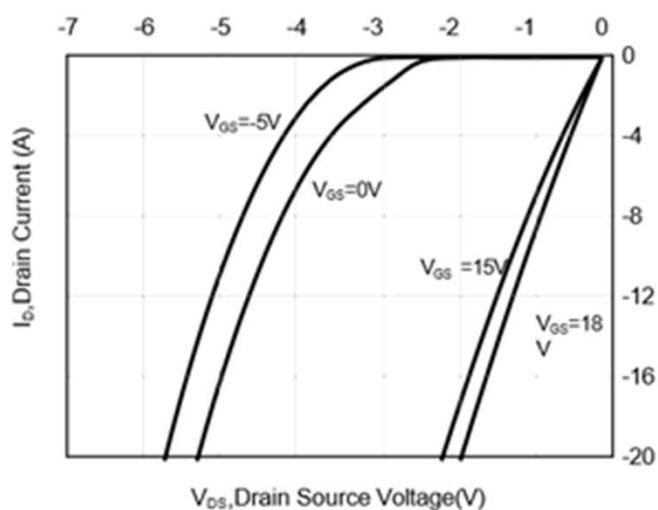


Fig 13. 3rd Quadrant Characteristic at $-55\text{ }^{\circ}\text{C}$

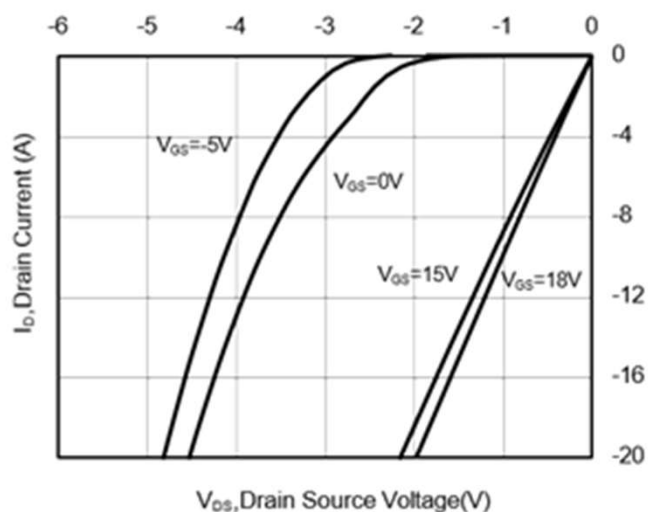


Fig 14. 3rd Quadrant Characteristic at $25\text{ }^{\circ}\text{C}$

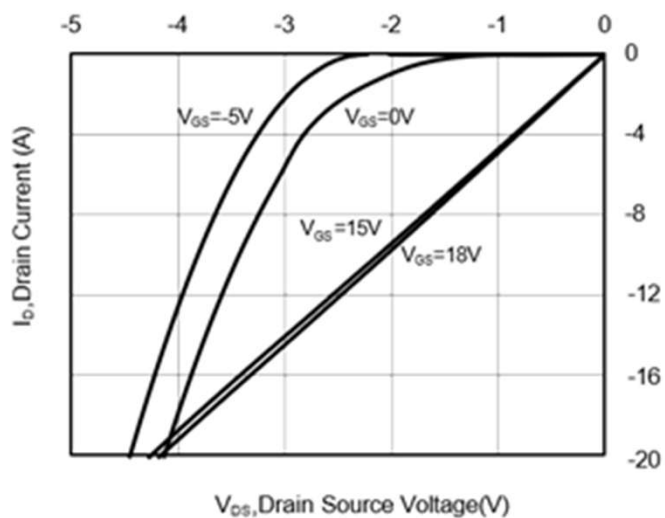
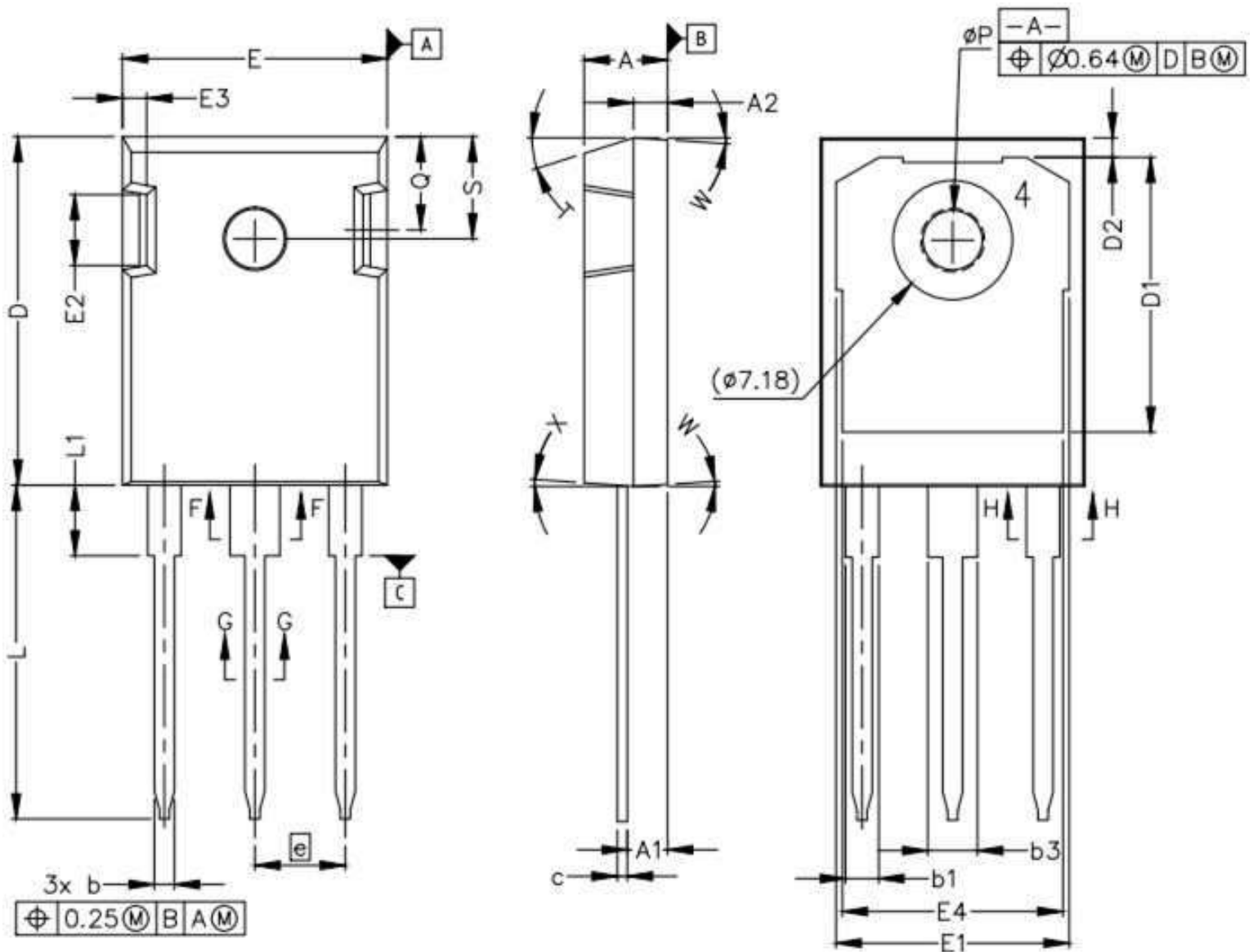


Fig 15. 3rd Quadrant Characteristic at $175\text{ }^{\circ}\text{C}$

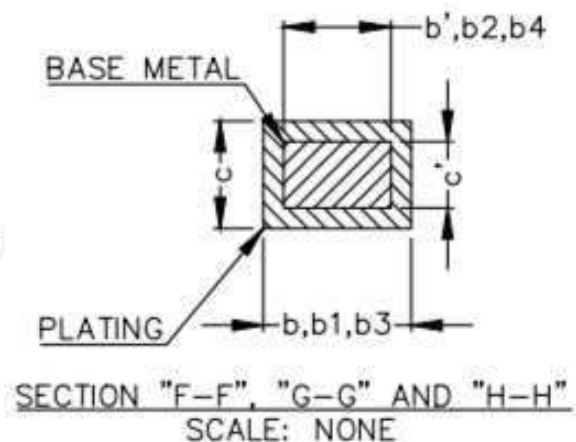
Package Outline: TO-247



NOTE:

1. ALL METAL SURFACES: TIN PLATED EXCEPT AREA OF CUT
2. DIMENSIONING & TOLERANCING CONFIRM TO ASME Y14.5M-1994.
3. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.
4. THIS DRAWING WILL MEET ALL DIMENSIONS REQUIREMENT OF JEDEC OUTLINES TO-247 AD.

- 1 - GATE
- 2 - DRAIN (COLLECTOR)
- 3 - SOURCE (EMITTER)
- 4 - DRAIN (COLLECTOR)



Package Outline: TO-247

SYMBOL	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	1.91	2.41
b2	1.91	2.16
b3	2.87	3.38
b4	2.87	3.13
c'	0.55	0.65
c	0.55	0.68
D	20.80	21.10
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	5.44 BSC	
N	3	
L	19.81	20.32
L1	4.10	4.40
ØP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF.	
W	3.5° REF.	
X	4° REF.	

Marking Information

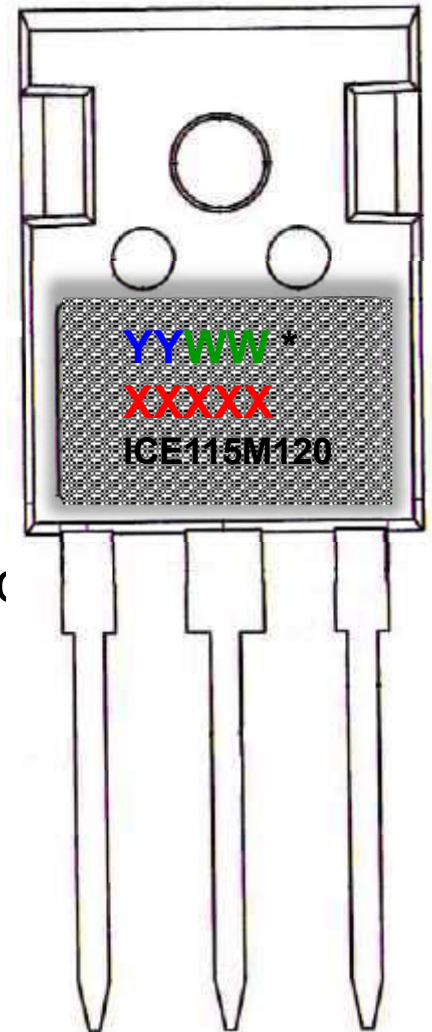
YY = Last two digits of the year

WW = Work week

***** = Site ID

XXXXXX = Lot ID

ICE115M120 = ICE is IceMOS logo and 115M120 is a designated device part number



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